## CLARKIN et al. -- Appln. No.: 09/771,569



- 4. (Amended) A device as in claim 1 or 3 with at least one drawn channel having a cross sectional area in the range of 0.0001mm<sup>2</sup> to 1mm<sup>2</sup>.
- 5. (Amended) A device as in claim 1 or 3 with at least one drawn channel having a length in the range of 1mm to 1km.
- 6. (Amended) A device as in claim 1 or 3, wherein the drawn substrate is utilized in a micro electro mechanical system.
- 7. (Amended) A device as in claim 1 or 3, wherein the drawn substrate is utilized in a lab on a chip system.

See the attached Appendix for changes made to effect the above claims.

Please enter new claims 38-41 as follows:



- --38. A device as in claim 1 or 3 with at least one drawn channel having a cross sectional area in the range of 0.0025mm<sup>2</sup> to 0.25mm<sup>2</sup>.--
- --39. A device as in claim 1 or 3 with at least one drawn channel having a cross sectional area in the range of 0.005mm<sup>2</sup> to 0.0075mm<sup>2</sup>.--
- --40. A device as in claim 1 or 3 with at least one drawn channel having a length in the range of 3mm to 1000mm.--
- --41. A device as in claim 1 or 3 with at least one drawn channel having a length in the range of 10mm to 250mm.--